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Sheet 1 of 1

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ATTY. DOCKET NO.

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APPLICATION NO.

10/682,378

INFORMATION DISCLOSURE STATEMENT
(Use several sheets if necessary)

APPLICANTS

Francesco Pappalardo et al.

FILING DATE

October 9, 2003

GROUP ART UNIT

2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CSK	AA	6,049,862	04/11/00	Bauer et al.	712	208	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK					
	AL					
	AM					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

CSK		Ertem, M., "A Reconfigurable Co-Processor for Microprocessor Systems," in <i>Proceedings of the Southeast Conference</i> , Tampa, April 5-8, 1987, New York, IEEE, vol. 1, pp. 225-228.
CSK		"Design Flexibility into Hardwired Logic," <i>IBM Technical Disclosure Bulletin</i> 37(3):321-324, March 1994.
CSK	AP	Hennessey, J. et al., <i>Computer Organization and Design. The Hardware/Software Interface</i> , Morgan Kaufman Publishers, San Francisco, 1994, Appendix C, "Mapping Control to Hardware", pp. C3-C28.
CSK	AQ	Jain, R., "An Alternative Approach Towards the Design of Control Units," <i>Microelectronics and Reliability</i> 24(6):1009-1012, 1984.
CSK	AR	"Opcode Compare Facility," Research Disclosure No. 315, Kenneth Mason Publications, p. 577, July 1990.

EXAMINER

Ken S. Kim

DATE CONSIDERED

3-1-06

* EXAMINER:

Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).